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[Data-parallel programming on MIMD computers](#)

PJ Hatcher, MJ Quinn, AJ Lapadula, BK SeEVERS, RJ ... - Parallel and Distributed Systems, IEEE Transactions on, 1991 - [ieeexplore.ieee.org](#)

... the efficient implementation of a data-parallel language ... points every physical processor can emulate ... Multicomputers synchronize through message passing, while ...

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[Universal programmable data communication connection system](#)

AS Brown, JT Hirni, BS Allen - US Patent 5,060,140, 1991 - [Google Patents](#)

... Socket: Endpoint of a connection at a softblock. Message: A data buffer transferred between processes on any processor in the system network. ...

[Cited by 51](#) - [Related articles](#) - [Web Search](#) - [All 3 versions](#)

[TreadMarks: Shared Memory Computing on Networks of Workstations](#) - [hawaii.edu](#) [PDF]

C Amza, AL Cox, S Dwarkadas, P Keleher, H Lu, R ... - COMPUTER, 1996 - [doi.ieeecomputersociety.org](#)

... involved false sharing, which occurs when two or more unrelated data objects are ...

P 2 writes to the page, it sends an invalidate message to processor P 1 . P ...

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[A Dynamic Operating System for Sensor Nodes](#) - [ucla.edu](#) [PDF]

CC Han, R Kumar, R Shea, E Kohler, M Srivastava - [usenix.org](#)

... SOS uses cooperative scheduling to share the processor between multiple lines ... by including a small buffer in the message header that the data payload can ...

[Cited by 163](#) - [Related articles](#) - [Web Search](#) - [All 19 versions](#)

[Parallel and Distributed Association Mining: A Survey](#) - [rpi.edu](#) [PS]

MJ Zaki - IEEE CONCURRENCY, 1999 - [doi.ieeecs.org](#)

... to access data in the local memory of another processor, message passing must send a copy of the desired data elements from one processor to the other ...

[Cited by 215](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 10 versions](#)

[The performance impact of flexibility in the Stanford FLASH multiprocessor](#) - [cornell.edu](#) [PS]

M Heinrich, J Kuskin, D Ofelt, J Heinlein, J ... - Proceedings of the sixth international conference on ..., 1994 - [portal.acm.org](#)

... For our 16-processor simulations, the average message requires latency ... For the ideal machine, the only latencies are those representing data transit or ...

[Cited by 131](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 15 versions](#)

[Reliable local data network arrangement](#)

RA Elliott, WV Lindquist - US Patent 4,692,918, 1987 - [freepatentsonline.com](#)

... of action independent of an associated host processor. ... with each other, not by storing data in a ... accessible memory, but by sending messages, comprising packets ...

[Cited by 137](#) - [Related articles](#) - [Web Search](#) - [All 3 versions](#)

[Supporting shared data structures on distributed memory architectures](#)

C Koelbel, P Mehrotra, J Van Rosendale - Proceedings of the second ACM SIGPLAN symposium on ..., 1990 - [portal.acm.org](#)

... processor directly by indexing into the processor array ... programmer can specify a data parallel algorithm at a ... note that there are no message passing statements ...

Cited by 166 - Related articles - Web Search - Library Search - All 4 versions

Active Messages: A Mechanism for Integrated Communication and Computation - ► [lamu.edu](#) (PDF)

T Eicken, DE Culler, SC Goldstein, KE Schauer - Computer Architecture, 1992. Proceedings., The 19th Annual ..., 1992 - [ieeexplore.ieee.org](#)

... in the ring communication example in Figure 2, data can be ... initiating a send in the user program on one processor to receiving the message in the ...

Cited by 1442 - Related articles - Web Search - Library Search - BL Direct - All 41 versions

Supporting dynamic data structures on distributed-memory machines

A Rogers, MC Carlisle, JH Reppy, LJ Hendren - ACM Transactions on Programming Languages and Systems (...), 1995 - [portal.acm.org](#)

... pointer-based dynamic data structures. ... memory machine is that each processor has its own ... references are satisfied through explicitly passed messages, which are ...

Cited by 191 - Related articles - Web Search - Library Search - BL Direct - All 6 versions

... interface with control processor & DMA controller for coupling data processing stations to common ...

TL Lillie - US Patent 4,441,162, 1984 - Google Patents

... to receive data, the communications control means ... signals the control processor upon detection of a message ... initializes the DMA controller, the message is received ...

Cited by 29 - Related articles - Web Search - All 2 versions

TreadMarks: distributed shared memory on standard workstations and operating systems - ► [epfl.ch](#)

(PDF)
P Keleher, AL Cox, S Dwarkadas, W Zwaenepoel - Proceedings of the USENIX Winter 1994 Technical Conference ..., 1994 - [portal.acm.org](#)

... eager software implementation of RC, a processor propagates its ... communication, because it requires a message to be ... data, while LRC propagates the data only to ...

Cited by 698 - Related articles - Web Search - Library Search - All 24 versions

Methods and apparatus for implementing a message driven processor in a client-server environment

DM Cloud, KF Kelly, DP Bonaccorsi, MK Weeks - US Patent 5,634,127, 1997 - [freepatentsonline.com](#)

... a particular type of request and a Message Data Context Area containing data related to the ... b. receiving from the message driven processor one or more ...

Cited by 47 - Related articles - Web Search - All 4 versions

Backup fault tolerant computer system

SD Glazer, J Baumbach, A Borg, E Wittels - US Patent 4,590,554, 1986 - Google Patents

... What is not shown is noted that the difference now is that the message queue up-to-date data space of the task processor 21 which is ...

Cited by 52 - Related articles - Web Search - All 3 versions

Tempest and typhoon: user-level shared memory - ► [wisc.edu](#) (PDF)

SK Reinhardt, JR Larus, DA Wood - Proceedings of the 21st annual international symposium on ..., 1994 - [portal.acm.org](#)

... nisms—low-overhead messages, bulk node-to-node data ... ways. Typhoon implements these mechanisms with a custom network interface processor; however, other ...

Cited by 376 - Related articles - Web Search - Library Search - BL Direct - All 25 versions

Integrating message-passing and shared-memory: early experience - ► [mit.edu](#) (PDF)

D Kranz, K Johnson, A Agarwal, J Kubiawicz, BH ... - ACM SIGPLAN Notices, 1993 - [portal.acm.org](#)

... coherent data transfer: A processor executing this operation describes a region

of memory on the local (source) node to be sent in a single message to some ...

[Cited by 123](#) - [Related articles](#) - [Web Search](#) - [Library Search](#) - [BL Direct](#) - [All 15 versions](#)

[Software Support for Virtual Memory-Mapped Communication](#) - [gatech.edu](#) (PDF)

C Dubnicki, L Iftode, EW Fellen, K Li - [Proceedings of IPPS'96](#) - [doi.ieeeecs.org](#)

... still useful for limiting the CPU overhead required ... provides fully protected, user-level message passing, and ... to initiate an outgoing block data transfer with ...

[Cited by 73](#) - [Related articles](#) - [Web Search](#) - [All 10 versions](#)

[BOOK] [The Duality of Memory and Communication in the Implementation of a Multiprocessor Operating System](#) - [wisc.edu](#) (PDF)

M Young, A Tevanian, R Rashid, D Golub, J Eppinger ... - [Computer Science Department, Carnegie-Mellon University](#)

... 11/787 (two processor 785) and 8204 (four processor 8200 ... and a variable-size collection

of typed data objects ... to manage port rights and control message reception ...

[Cited by 215](#) - [Related articles](#) - [Web Search](#) - [Library Search](#) - [All 48 versions](#)

[The Stanford FLASH multiprocessor](#) - [mdh.se](#) (PDF)

J Kuskín, D Ofelt, M Heinrich, J Heinlein, R ... - [International Symposium on Computer Architecture, 1998](#) - [portal.acm.org](#)

... To improve performance, PP code and data are cached ... Finally, the outbox handles outgoing

message sends on behalf ... O for delivery to the processor, network, and ...

[Cited by 665](#) - [Related articles](#) - [Web Search](#) - [BL Direct](#) - [All 27 versions](#)

[CITATION] [DATA DIFFUSION MACHINE A SCALABLE SHARED VIRTUAL MEMORY MULTIPROCESSOR](#)

DHD Warren, S Haridi - [Fifth generation computer systems 1988: Proceedings of the ...](#), 1988 - [Springer-Verlag Berlin](#)

and Heidelberg GmbH & Co. K

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Key authors: [P Keleher](#) - [A Cox](#) - [S Dwarkadas](#) - [W Zwaenepoel](#) - [S Reinhardt](#)

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